IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

Art Unit:

Not assigned

Takahiro IMAI

Examiner: Not assigned

Serial No: Not assigned

Filed: September 12, 2003

For:

Semiconductor Device and Method of Manufacturing the

Same, Circuit Board, and Electronic Instrument

TRANSMITTAL OF INFORMATION DISCLOSURE **STATEMENT**

Mail Stop PATENT APPLICATION Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Dear Sir:

The information disclosure statement submitted herewith is being filed concurrently with the subject application [37 C.F.R. § 1.97(b)] and contains no items of information cited in any communication from a foreign patent office in a counterpart foreign application [37 C.F.R. § 1.97(e)(1)].

If it should be determined that for any reason either an insufficient or excessive fee has been paid, please charge any insufficiency or credit any overpayment necessary to ensure consideration of the information disclosure statement for the above-identified application to Deposit Account No. 50-1314. A copy of this paper is enclosed.

Respectfully submitted,

Date: September 12, 2003

Anthony, J/Orler

Registration No. 41,232

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Facsimile: 213-337-6701

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FORM PTO-1449			Docket Number (Optional) 81751.0065		Application Number Not assigned	
INFORMATION DISCLOSURE CITATION IN AN APPLICATION			Applicant Takahiro IMAI			
(Use several sheets if necessary)			Filing Date September 12, 2003		Group Art Unit Not assigned	
U.S. PATENT DOCUMENTS						
EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
						
				-		
				-		
				-		
FOREIGN PATENT DOCUMENTS						
	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION (YES/NO)
	08-125077	05/17/96	Japan			Abstract
OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)						
						
						
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